

Exposition and Comparison of Two Kinds of *a Posteriori* Analysis of Fault Trees

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Abstract. Fault trees are top-down formal deductive analytic tools with diverse applications in many fields such as reliability, safety and security. Forward fault tree analysis (FTA) can be termed *a priori* analysis since it predicts the top-event probability in terms of basic-event probabilities. This paper offers a tutorial exposition and a detailed comparison of two kinds of backward or *a posteriori* FTA that are implemented in the probability domain and in the Boolean domain, respectively. For the probability-domain *a posteriori* FTA, it is assumed that the top event probability is known. For example, when the top event is presumed to have occurred, then it has a probability of one. The analysis proceeds recursively in the probability domain to assess the probabilities of lower events under certain realistic assumptions such as mutual exclusiveness or statistical independence of the input events for a specific gate, and with the utilization of educated guesses on certain ratios of probabilities of such events. This paper offers a detailed mathematical procedure for implementing this *a posteriori* FTA that makes the most of the concept of duality. The procedure is demonstrated via a detailed illustrative example. The paper also considers the *a posteriori* FTA in the Boolean domain. Such an analysis is available in the literature in terms of the very powerful tool of *Bayesian Networks* (BNs). We demonstrate here that in many cases this analysis is still possible via elementary fault-tree manipulations that use the concept of a *Boolean quotient* to effectively implement Bayes' Theorem in the Boolean domain. Again, a demonstrative example is given to illustrate the Boolean *a posteriori* FTA, explain its details, and show that the power of BNs is not really warranted in simple cases. A detailed comparison between the two kinds of *a posteriori* FTA is also given to identify their similarities and differences.

Keywords: Fault tree; *a priori* analysis; *a posteriori* analysis; probability domain; Boolean domain.

1. Introduction

Fault trees are top-down formal deductive analytic tools that have applications in many fields such as reliability, safety, and security, albeit sometimes they are used under a variety of unwarranted disguised names such as success trees, elicitation trees, attack trees, defense trees, *etc.* Conventional fault tree analysis (FTA) might be termed a forward, *a priori*, or predictive analysis since it obtains the top-event probability in terms of basic-event probabilities,

or more generally it decides the probability of any higher-level event in the tree in terms of the probabilities of its lower-level events^[1-20]. The reverse type of FTA might be termed a backward, *a posteriori*, or diagnostic analysis. There are (at least) two kinds of this analysis, which are to be reviewed, analyzed, demonstrated, compared and interrelated in this paper.

The first kind of *a posteriori* FTA is due to Shooman^[21, 22]. In this kind of analysis, it is assumed that the top event has a known

probability, *e.g.*, the top event could be considered to have actually occurred and hence possess a probability of one. The analysis proceeds recursively in the probability domain to assess the probabilities of lower events under certain realistic assumptions such as mutual exclusiveness or statistical independence of the input events for a specific gate, and utilization of educated guesses on certain ratios of probabilities of such events. By contrast, the second kind of *a posteriori* analysis, as reported by Bobbio, *et al.*,^[23] and Langseth and Portinale^[24] is in essence a classical Bayesian analysis involving an equivalent of the Total Probability Theorem, and Bayes' Theorem^[25]. A detailed comparison between the two kinds of *a posteriori* analysis is given in Table 1. Various aspects of the comparison in Table 1 will become clarified further as we proceed throughout this paper.

While Shooman^[22] restricted the *a posteriori* FTA to that of OR gates with Mutually Exclusive (ME) inputs, we extend the analysis to include both AND gates with Statistically Independent (SI) inputs and OR gates with either ME or SI inputs. We derive general solutions for all types of gates and conditions with arbitrary numbers of inputs. We also outline the solution of the general case based on the use of the Inclusion-Exclusion Principle with simplifying assumptions other than the ME or SI assumptions.

The second kind of *a posteriori* analysis is typically conducted by mapping fault trees into the more powerful tool of Bayesian networks (BNs), which are known also (occasionally with minor differences) as belief nets, causal networks, probabilistic-dependence graphs, or influence diagrams. Bayesian networks have

better capabilities than standard fault trees, such as their capabilities to handle uncertainty, statistical dependence or multi-state behavior^[23, 24, 26-39]. However, the use of BNs in *a posteriori* FTA might not be warranted in many important problems that can still be handled via (the somewhat modest) capabilities of fault trees.

The organization of the remainder of this paper is as follows. Section 2 lists our notation, abbreviations and certain useful nomenclature. Section 3 presents the *a posteriori* analysis of fault trees in the probability domain. The main thesis of this section is that such an analysis necessitates only the *a posteriori* analysis of single gates. Therefore, section 3 discusses the general *a posteriori* analysis of single AND or OR gates, and then derives (under a variety of appropriate assumptions) *a posteriori* solution for an AND gate with SI inputs, an OR gate with ME inputs, and an OR gate with SI inputs. The results obtained are applied to a detailed fault-tree example. Section 4 treats the *a posteriori* analysis of fault trees in the Boolean domain. We demonstrate here that in many cases this analysis is possible via elementary fault-tree manipulations that use the concept of a Boolean quotient (known also as a Boolean ratio, subfunction or restriction)^[40-52] to implement Bayes' Theorem effectively in the Boolean domain. Again, a demonstrative example is given to illustrate the Boolean *a posteriori* FTA and explain its details, and show that the power of BNs is not really warranted in simple cases. A detailed comparison between the two kinds of *a posteriori* FTA is also given with the hope of setting the stage on how they can be further interrelated and even combined. Section 5 concludes the paper and points out new directions for further research.

Table 1. Comparison of the two kinds of a posteriori analysis of fault trees.

	First kind	Second kind
Basic assumption	Expert guessing of certain ratios among probabilities of inputs of various gates.	Knowledge of basic-event <i>a priori</i> probabilities
Nature of relation considered	<i>Local</i> gate relations between the probabilities of the output and input of <i>single</i> gates	An <i>overall</i> tree relation between the top-event probability and basic-event probabilities
Forward analysis incorporated ?	No	Yes
Mathematics needed	Solution of algebraic equations (essentially quadratic equations)	Classical Bayesian analysis involving an equivalent of the Total Probability Theorem, and Bayes' Theorem
Implementation via Bayesian Networks	No	Possible (Necessary only to avoid limitations of FTAs)
Utility as an aid to <i>a priori</i> analysis	Yes	No
Utility as an aid to guessing input probability ratios	No	Yes
Typical applications	Forensic analysis of terrorist attacks	Diagnosis of safety-critical systems
Seminal work	Shooman ^[22]	Bobbio, <i>et al.</i> , ^[23] ; Langseth and Portinale ^[24]

2. Notation, Abbreviations and Nomenclature

A. Notation

$P(A)$	=	Probability of the event A.
r_i	=	Ratio of $P(A_i)$ to $P(A_n)$ for $i = 1, 2, \dots, n$, $r_n = 1$.
$E \{ \dots \}$	=	Expectation or expected value of a random variable $\{ \dots \}$.
e_i	=	A probabilistic event; input i of an AND or an OR gate.
a_n	=	A probabilistic event; output of an AND gate of n inputs.
o_n	=	A probabilistic event; output of an OR gate of n inputs.
R_{n-1}	=	Ratio of $P \left(\bigcup_{i=1}^{n-1} A_i \right)$ to $P(A_n)$.
e_X	=	A fault-tree event labelled by indicator variable X .
t_i	=	Ratio of $P(\bar{A}_i)$ to $P(\bar{A}_n)$ for $i = 1, 2, \dots, n$, $t_n = 1$.
T	=	Particular name for the indicator variable of the top event e_T of the fault tree.
X	=	Generic name for the indicator variable of a certain FT event e_X . This is a random Boolean (switching) variable such that: $X = 1$ ($\bar{X} = 0$) if the event e_X occurs, and $X = 0$ ($\bar{X} = 1$) if the event e_X does not occur.
$x = E\{X\}$	=	Expectation of the indicator variable X given by $x = E\{X\} = (1)P(X) + (0)P(\bar{X}) = P(X)$, <i>i.e.</i> , it is equal to the probability of occurrence of event e_X .

B. Abbreviations

FTA	Fault-Tree Analysis,
ME	Mutually Exclusive(ness),
SI	Statistically Independent/Statistical Independence,
BN	Bayesian Network.

C. Nomenclature

Forward (*a priori* or a predictive) fault tree analysis:

A fault-tree analysis in which the basic-event probabilities are known. The analysis chains forward to obtain higher-level event probabilities and terminates with a prediction of the top-event probability. This is the conventional fault-tree analysis, and it is what is meant when simply fault-tree analysis is mentioned.

Backward (*a posteriori* or diagnostic) fault tree analysis:

A fault-tree analysis in which the top-event probability is known. This analysis is mainly used when the top event is assumed to have occurred and hence has a probability of one.

***A posteriori* FTA of the first kind:**

A fault-tree analysis that chains backward to obtain lower-level event probabilities (under certain realistic assumptions), and terminates with a knowledge of all basic-event probabilities. The analysis relies on the solution of algebraic equations expressing probabilities of the inputs of a certain gate in terms of the probability of its output. Such a solution proceeds recursively from the top gate (whose output has a known probability, typically one) to lower-level gates terminating at the leaf gates. Typically, the analysis relies on the expert guessing of certain ratios among probabilities of various gates.

***A posteriori* FTA of the second kind:**

A fault-tree analysis that starts with a priori knowledge of basic-event probabilities, utilizes this knowledge in forward analysis to compute the top-event probability, and then (under the assumption that the top event has occurred) uses Bayes' theorem to deduce the *a posteriori* basic-event probabilities.

Bayesian Network (BN):

A directed acyclic graph in which discrete random variables are assigned to each node, together with the conditional dependence on the parent nodes. Root nodes are nodes with no parents, and marginal prior probabilities are assigned to them. The main feature of a BN is that it is possible to include local conditional dependencies into the model, by directly specifying the causes that influence a given effect. Bayesian Networks^[23, 24] are usually defined on discrete random variables, though some extensions have been proposed for extending the formalism to some form of continuous random variables. BN are more suitable to represent complex dependencies among components and to include uncertainty and multi-state behavior in modeling^[23, 24].

Mapping BNs into FTs:

It is quite straightforward to map a given FT into an equivalent BN with binary nodes, where the FT's gates (with input and output events) are mapped into small BN fragments, whose combination produces the whole BN corresponding to the given FT. In other words,

the modular construction of an FT can be mapped into a modular construction of an equivalent BN. The modeling flexibility of the BN formalism can accommodate various kinds of statistical dependencies Uncertainties, and multi-state behavior that are difficult to include in the FT formalism^[23, 24].

Reliability-Ready Expression (RRE): An expression in the switching (Boolean) domain, in which logically multiplied (ANDed) entities are statistically independent and logically added (ORed) entities are disjoint. Such an expression can be directly transformed, on a one-to-one basis, to the algebraic or probability domain by replacing switching (Boolean) indicators by their statistical expectations, and also replacing logical multiplication and addition (ANDing and ORing) by their arithmetic counterparts Rules for the conversion of a general switching (Boolean) expression into a PRE are provided in^[8, 9, 52-56].

Duality:

The dual of a switching function is obtained by complementing the function and all its

switching arguments (inverting both output and inputs)^[56-58].

3. The *a Posteriori* Analysis in the Probability domain

Since the *a posteriori* analysis of a fault tree can be accomplished in terms of that of single gates, this section is devoted to the *a posteriori* analysis of single AND or OR gates, first generally, and then subject to the Mutual Exclusiveness (ME) or Statistical Independence (SI) assumptions. The analysis technique is then demonstrated via a detailed numerical example.

3.1. General Analysis of AND and OR gates

The aim of this subsection is to discuss the general analysis of AND and OR gates, stress the utility of the concept of *duality* in such analysis, and point out the considerable reduction in complexity when the inputs are either Mutually Exclusive (ME) or Statistically Independent (SI).

The output a_n of an AND gate of n inputs e_1, e_2, \dots, e_n has a probability given in terms of conditional probabilities as^[25]

$$P(a_n) = P\left(\prod_{i=1}^n e_i\right) = P(e_1)P(e_2|e_1)P(e_3|e_1e_2) \dots P(e_n|e_1e_2 \dots e_{n-1}), \quad (1)$$

while the output o_n of an OR gate of n inputs e_1, e_2, \dots, e_n has a probability given by the Inclusion-Exclusion Principle^[25, 59, 60].

$$P(o_n) = P\left(\bigcup_{i=1}^n e_i\right) = \sum_{i=1}^n P(e_i) - \sum \sum_{1 \leq i < j \leq n} P(e_i \cap e_j) + \sum \sum \sum_{1 \leq i < j < k \leq n} P(e_i \cap e_j \cap e_k) - \dots + (-1)^{n-1} P\left(\prod_{i=1}^n e_i\right). \quad (2)$$

Note that (2) expresses the output of an OR gate in terms of the outputs of many binary or multi-input AND gates, which need to be expressed via (1) or extensions thereof. The AND and OR

gates are dual gates. Complementation of both inputs and output of one gate produces the other gate. This is the essence of the two De Morgan's laws, visually represented by Fig. 1, and mathematically given by

$$\left\{o_n = \bigcup_{i=1}^n e_i\right\} \Leftrightarrow \left\{\bar{o}_n = \prod_{i=1}^n \bar{e}_i\right\}, \quad (3)$$

$$\left\{ a_n = \bigcap_{i=1}^n e_i \right\} \Leftrightarrow \left\{ \bar{a}_n = \bigcup_{i=1}^n \bar{e}_i \right\}. \quad (4)$$

According to (3) and (4), the analysis of an AND (OR) gate can be converted to the dual analysis of an OR (AND) gate. Therefore, the analyst has a choice to analyze any given gate directly as is or indirectly in terms of its dual gate.

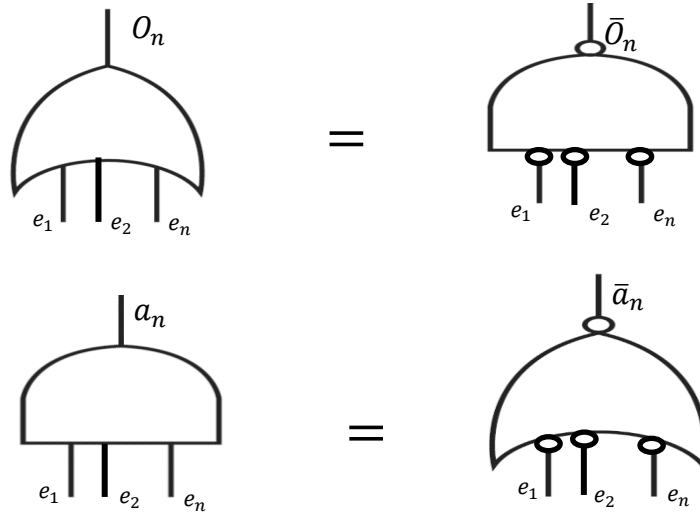


Fig. 1. Visual Interpretation of De Morgan's Laws.

The analysis of an AND gate via (1) requires the use of conditional probabilities, while the analysis of an OR gate via (2) involves an exponential number ($2^n - 1$) of terms, many of which necessitate the use of conditional probabilities in expressions similar to (1). There

is a considerable reduction in the complexity of the analysis when the events e_i are either Mutually-Exclusive (ME) or Statistically Independent (SI).

If the events e_i are ME, *i.e.*, if

$$e_i \cap e_j = \emptyset \quad \forall i \text{ and } j, \quad (5)$$

then (1) and (2) reduce respectively to

$$P(a_n) = 0, \text{ ME } e_i, \quad (6)$$

$$P(o_n) = P\left(\bigcup_{i=1}^n e_i\right) = \sum_{i=1}^n P(e_i), \text{ ME } e_i. \quad (7)$$

If, instead the events e_i are SI, *i.e.*, if

$$P(e_i|e_j) = P(e_i), \quad \forall i \text{ and } j, \quad (8)$$

or equivalently, if

$$P(e_i \cap e_j) = P(e_i) P(e_j), \quad (9)$$

then (1) and (2) reduce respectively to

$$P(a_n) = \prod_{i=1}^n P(e_i), \quad SI \ e_i. \quad (10)$$

$$\begin{aligned} P(o_n) = & \sum_{i=1}^n P(e_i) - \sum_{1 \leq i < j \leq n} P(e_i)P(e_j) + \sum_{1 \leq i < j < k \leq n} P(e_i)P(e_j)P(e_k) - \dots \\ & + (-1)^{n-1} \prod_{i=1}^n P(e_i) = 1 - \prod_{i=1}^n [(1 - P(e_i))], SI \ e_i. \end{aligned} \quad (11)$$

Note that (11) can also be obtained from (3) and (10) in the equivalent complementary form

$$P(\bar{o}_n) = \prod_{i=1}^n P(\bar{e}_i), SI \ e_i. \quad (12)$$

3.2. Analysis of an AND gate with SI inputs

We assume that the probability of the output a_n of the AND gate is known, say S_n . This probability is exactly 1 if the event a_n is known to have occurred. Otherwise, it would be

available through expert estimation or through a *posteriori* analysis of higher-level gates. Since the inputs of the AND gate are SI, equation (10) is applicable and reduces to

$$\prod_{i=1}^n P(e_i) = S_n, \quad SI \ e_i. \quad (13)$$

Following Shooman^[9], we assume that we can express each of the probabilities in (13) as a ratio r_i of the last probability among them $P(e_n)$, namely

$$P(e_i) = r_i P(e_n), \quad 1 \leq i \leq n, \quad (14)$$

where $r_n = 1$. Substituting (14) in (13), we solve (13) for each of the probabilities $P(e_i)$ as

$$P(e_i) = r_i \left[\left(\prod_{j=1}^n r_j \right)^{-1} S_n \right]^{1/n}, \quad 1 \leq i \leq n, \quad SI \ e_i. \quad (15)$$

3.3. Analysis of an OR gate with ME Inputs

The case studied in this subsection is the only case studied by Shooman^[22]. Here, equation (7)

is applicable, and the probability of the output T_n of the OR gate is known, say S_n . Hence, equation (7) can be rewritten as:

$$\sum_{i=1}^n P(e_i) = S_n, ME e_i. \quad (16)$$

Now, making the assumption (14) and substituting (14) in (16), we can solve (16) for each of the probabilities $P(e_i)$ as:

$$P(e_i) = S_n r_i \left[\sum_{j=1}^n r_j \right]^{-1}, \quad 1 \leq i \leq n, ME e_i. \quad (17)$$

Note that (17) for the ME inputs of OR has some resemblance with (15) for the SI inputs of AND.

3.4. Analysis of an OR gate with SI inputs

The OR gate with SI inputs is analyzed in a direct fashion in subsection 3.4.1 and is analyzed via its dual representation in subsection 3.4.2.

3.4.1. Direct Analysis

The output of an OR gate with n inputs can be written as:

$$o_n = \bigcup_{i=1}^n e_i = o_{n-1} \cup e_n, \quad (18)$$

where

$$o_{n-1} = \bigcup_{i=1}^{n-1} e_i. \quad (19)$$

Since the event e_n is statistically independent of each of the events e_i ($1 \leq i \leq n-1$), then it is also independent of their union o_{n-1} . The expression (18) allows the Inclusion-Exclusion Principle (2) to be rewritten as:

$$S_n = P(o_n) = P(o_{n-1}) + P(e_n) - P(o_{n-1})P(e_n). \quad (20)$$

Now, we assume that we can express $P(o_{n-1})$ as a ratio R_{n-1} of $P(e_n)$, *i.e.*

$$P(o_{n-1}) = R_{n-1} P(e_n), \quad (21)$$

and hence obtain the following quadratic equation in $P(e_n)$

$$R_{n-1}[P(e_n)]^2 - (1 + R_{n-1})P(e_n) + S_n = 0. \quad (22)$$

Equation (22) has two solutions:

$$P(e_n) = \frac{1}{2R_{n-1}} [(1 + R_{n-1}) \mp \sqrt{D}], \quad (23)$$

where the discriminant D is

$$\begin{aligned} D &= (1 + R_{n-1})^2 - 4R_{n-1}S_n \\ &= 1 + R_{n-1}^2 + 2R_{n-1} - 4R_{n-1}S_n \\ &\geq (2R_{n-1}) + 2R_{n-1} - 4R_{n-1}S_n \\ &= 4R_{n-1}(1 - S_n) \geq 0. \end{aligned} \quad (24)$$

In (24), we made use of the fact that S_n is a probability and hence must be less than or equal to 1. Equation (24) indicates that the discriminant D is non-negative, and hence both roots in (22) are *real*. Equation (24) also indicates that

$$\sqrt{D} \leq (1 + R_{n-1}), \quad (25)$$

and hence both roots in (23) are *positive*. However, we now reject the positive sign in (23) since it corresponds to the solution

$$P(e_n) = \frac{1}{2R_{n-1}} [(1 + R_{n-1}) + \sqrt{D}], \quad (26a)$$

$$P(o_{n-1}) = \frac{1}{2} [(1 + R_{n-1}) + \sqrt{D}], \quad (26b)$$

which corresponds to a probability $P(e_n) > 1$ if $R_{n-1} < 1$, and to a probability $P(o_{n-1}) > 1$ if $R_{n-1} > 1$. The only possibility of accepting the positive sign in (23) is the trivial case $R_{n-1} = 1$, $S_n = 1$ for which D is 0 and the two roots in (22) are equal. Hence, our final solution of (22) is

$$P(e_n) = \frac{1}{2R_{n-1}} \left[(1 + R_{n-1}) - ((1 + R_{n-1})^2 - 4R_{n-1}S_n)^{\frac{1}{2}} \right], \quad (27a)$$

$$P(o_{n-1}) = \frac{1}{2} \left[(1 + R_{n-1}) - ((1 + R_{n-1})^2 - 4R_{n-1}S_n)^{\frac{1}{2}} \right]. \quad (27b)$$

Now we use S_{n-1} to denote $P(o_{n-1})$ and continue our work recursively to obtain the probabilities $P(e_{n-1}), P(e_{n-2}), \dots, P(e_1)$. Figure 2 summarizes the previous computations in flow-chart form.

3.4.2. Dual Analysis

An alternative analysis of an OR gate with SI inputs is possible via equation (12). Now, we assume that each of the probabilities of the complementary events in (12) is expressed as a ratio t_i of the last probability among them $P(\bar{e}_n)$, i.e.,

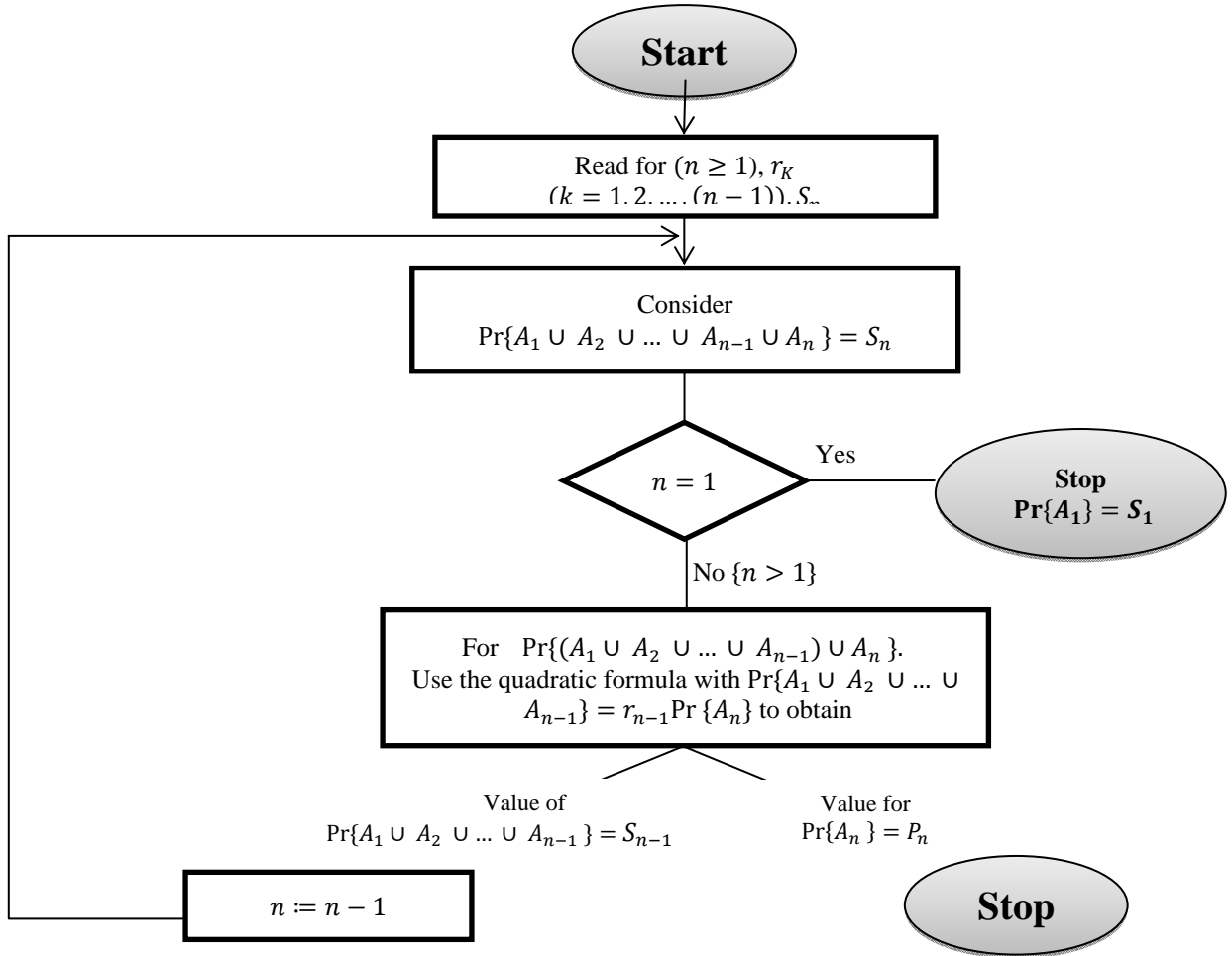


Fig. 2. Assigning probabilities for n statistically-independent inputs of an OR gate given the probability of its output.

$$P(\bar{e}_i) = t_i P(\bar{e}_n), \quad 1 \leq i \leq n, \quad (28)$$

where $t_n = 1$. Equating the RHS of (12) to S_n and substituting (28) into the resulting equation, we can solve (12) for each of the complementary probabilities $P(\bar{e}_i)$ as

$$P(\bar{e}_i) = t_i \left[\left(\prod_{j=1}^n t_j \right)^{-1} (1 - S_n) \right]^{1/n}, 1 \leq i \leq n, \text{ SI } \bar{e}_i. \quad (29)$$

In passing, we note that we used the assumption (28) to obtain a simple solution. Had we insisted on using the assumption (14), we would have obtained an n th – degree equation in each $P(e_i)$. The alternative (equally good) assumption in (28) saved us the trouble of solving an n th degree polynomial equation and the associated difficulty of selecting the appropriate root from a set of n roots.

Example 1:

Figure 3 displays a fault tree that combines all the special cases considered. It has an OR gate with three ME inputs, an AND gate with three SI inputs, and an OR gate with three SI inputs. Let us assume that the top event probability $P(o_3)$ is known to be $S_3 = 0.9$. We need to find all the basic-event probabilities. We start by estimating the probabilities of the events $e_1, e_2,$ and e_3 which are the ME inputs of the top OR gate. We now assume we know the following probability ratios.

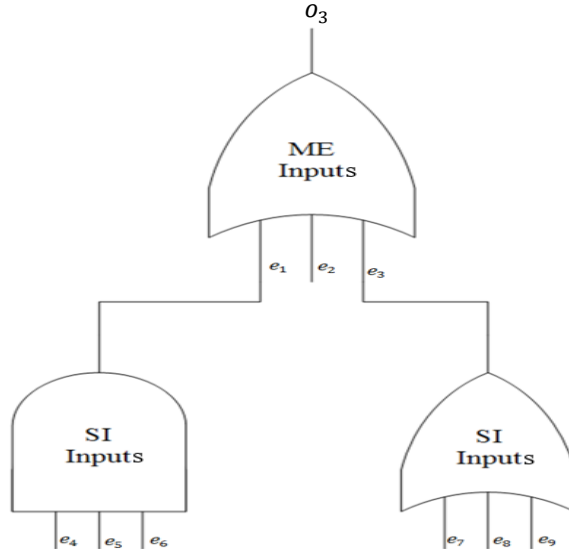


Fig. 3. A simple example of a fault tree that has an OR gate with MI inputs, an AND gate with SI input and an OR gate with SI inputs.

$$r_1 = P(e_1)/P(e_3) = 0.2, \quad (30a)$$

$$r_2 = P(e_2)/P(e_3) = 0.3, \quad (30b)$$

$$r_3 = P(e_3)/P(e_3) = 1.0. \quad (30c)$$

Hence, according to (17), we obtain

$$P(e_1) = \frac{S_3 r_1}{r_1 + r_2 + r_3} = \frac{(0.9)(0.2)}{0.2 + 0.3 + 1.0} = 0.12, \quad (31a)$$

$$P(e_2) = \frac{S_3 r_2}{r_1 + r_2 + r_3} = 0.18, \quad (31b)$$

$$P(e_3) = \frac{S_3 r_3}{r_1 + r_2 + r_3} = 0.60, \quad (31c)$$

As expected $P(e_1)$, $P(e_2)$ and $P(e_3)$ are in the ratio of 0.2: 0.3: 1.0 and add up to $S_3 = 0.9$. We now know the probability of the output e_1 of the AND gate, and need to assess the probabilities of its inputs e_4 , e_5 , and e_6 . Again we assume we know the following probability ratios.

$$r_4 = P(e_4)/P(e_6) = 0.4, \quad (32a)$$

$$r_5 = P(e_5)/P(e_6) = 0.5, \quad (32b)$$

$$r_6 = P(e_6)/P(e_6) = 1.0. \quad (33c)$$

Hence, according to (15), we obtain

$$P(e_4) = r_4 [P(e_1)/(r_4 r_5 r_6)]^{\frac{1}{3}} = 0.33737, \quad (33a)$$

$$P(e_5) = r_5 [P(e_1)/(r_4 r_5 r_6)]^{\frac{1}{3}} = 0.42172, \quad (33b)$$

$$P(e_6) = r_6 [P(e_1)/(r_4 r_5 r_6)]^{\frac{1}{3}} = 0.84343. \quad (33c)$$

As expected $P(e_4)$, $P(e_5)$ and $P(e_6)$ are (to within roundoff-errors) in the ratio 0.4: 0.5: 1.0 and their product is 0.12. Likewise, we use our knowledge of the probability of the output e_3 of the OR gate with *SI* inputs e_7 , e_8 , e_9 to estimate the probabilities of these inputs. We use the dual analysis in Sec. 3.4.2, and starting with $P(\bar{e}_3) = 0.4$, we obtain $P(\bar{e}_7)$, $P(\bar{e}_8)$ and $P(\bar{e}_9)$. We assume we know the probability ratios

$$t_7 = P(\bar{e}_7)/P(\bar{e}_9) = 0.6, \quad (34a)$$

$$t_8 = P(\bar{e}_8)/P(\bar{e}_9) = 0.7, \quad (34b)$$

$$t_9 = P(\bar{e}_9)/P(\bar{e}_9) = 1.0, \quad (34c)$$

Hence, according to (29), we obtain

$$P(\bar{e}_7) = t_7 [P(\bar{e}_3)/(t_7 t_8 t_9)]^{\frac{1}{3}} = 0.59032, \quad (35a)$$

$$P(\bar{e}_8) = t_8 [P(\bar{e}_3)/(t_7 t_8 t_9)]^{\frac{1}{3}} = 0.68871, \quad (35b)$$

$$P(\bar{e}_9) = t_9 [P(\bar{e}_3)/(t_7 t_8 t_9)]^{\frac{1}{3}} = 0.98387, \quad (35c)$$

As expected, $P(\bar{e}_7)$, $P(\bar{e}_8)$, and $P(\bar{e}_9)$ are (to within roundoff-error) in the ratio 0.6: 0.7: 1.0 and their product is 0.4. The original probabilities are $P(e_7) = 0.40968$, $P(e_8) = 0.31129$, and $P(e_9) = 0.01613$.

4. The *a Posteriori* Analysis in the Boolean Domain

In this section, we demonstrate how to apply Bayes' theorem to achieve a *posteriori* FTA via manipulations in the Boolean domain. Let the top event be denoted by e_T and a basic event be denoted by e_X , then Bayes' Theorem^[25] states that

$$P\{e_X|e_T\} = P\frac{\{e_T \cap e_X\}}{\{e_T\}} P\{e_T\}, \quad (36)$$

provided $P\{e_T\} \neq 0$. This theorem can be restated in terms of the indicator variables T and X of the events e_T and e_X when noting that the various probabilities in (36) can be rewritten as expectations, *i.e.*,

$$P\{e_x|e_T\} = E\{X|T\}, \quad (37a)$$

$$P(e_T \cap e_X) = E\{T \wedge X\}, \quad (37b)$$

$$P\{e_T|e_X\} = E\{T|X\}, \quad (37c)$$

So that (36) can be rewritten as

$$P\{e_X|e_T\} \equiv E\{X|T\} = E\{T \wedge X\} / E\{T\}. \quad (38)$$

Equation (38) is valid provided $E\{T\} \neq 0$. Now we can obtain the *a posteriori* probability $P\{e_X|e_T\}$ by pursuing the following steps:

1. Express T as a PRE (preferably the simplest possible) as a Boolean function of indicator variables (including X). Note that the job of forming a PRE is needed only once (at this initial step).
2. The indicator variable $(T|X)$ is the Boolean quotient of T with respect to X , *i.e.*

$$T|X = T/X = T]_{X=1}, \quad (39)$$

Further information on the Boolean quotient is given in Appendix A. Since T is in PRE form, each $(T|X)$, for any choice of X , is also in PRE form.

3. The indicator variable $(T \wedge X)$ is obtained via (A4) as

$$(T \wedge X) = X \wedge (T|X). \quad (40)$$

Since $(T|X)$ is independent of X and is in PRE form, then $X \wedge (T|X)$ is also in PRE form.

4. The expectations $E\{T \wedge X\}$ and $E\{T\}$ in the RHS of (38) are now obtained immediately as one -to- one

transformations of the PREs for $(T \wedge X)$ and (T) .

The details of this method is now illustrated by applying it to a fault-tree example studied via Bayesian Networks by Bobbio, *et al.*,^[23].

Example 2:

This example, originally taken from Malhotra and Trivedi^[61], deals with the fault tree shown in Fig. 4. Bobbio, *et al.*,^[23] solve this example by mapping the fault tree into a Bayesian network. We will demonstrate that such a mapping is not really warranted since fault-tree techniques suffice in this case. The fault tree represents a redundant multiprocessor system, with a single bus N connecting two processors P_1 and P_2 having access to a local memory bank each (M_1 and M_2), and through the bus to a shared memory bank M_3 , so that if the local memory bank fails, the processor can use the shared one. Each processor is connected to a mirrored disk unit. If one of the disks fails, the processor switches on the mirror. The whole system is functional if the bus N is functional and one of the processing subsystems is functional. With a little abuse of notation, we are using the same upper-case un-complemented literal to denote a component, and also to denote the indicator variable for its failure. We can write the indicator T for the top event as a disjunction of cutset failures as in (3) of Bobbio, *et al.*,^[23], but if we do so, we lose the ability to utilize statistical independence among basic events and end up with a complicated expression for the top-event probability. Instead, we write T as

$$T = N \vee (S_1 S_2), \quad (41)$$

where S_1 and S_2 are given by

$$S_1 = P_1 \vee D_{11}D_{12} \vee M_1M_3, \quad (42)$$

$$S_2 = P_2 \vee D_{21}D_{22} \vee M_2M_3, \quad (43)$$

We note that S_1 and S_2 would have been statistically independent had there been no common element M_3 between them. To circumvent this problem, we use a Boolean-Shannon expansion about M_3 to obtain

$$\begin{aligned}
 S_1 S_2 &= \bar{M}_3 (S_1 S_2 | 0_{M_3}) \vee M_3 (S_1 S_2 | 1_{M_3}) \\
 &= \bar{M}_3 (P_1 \vee D_{11} D_{12}) (P_2 \vee D_{21} D_{22}) \\
 &\quad \vee M_3 (P_1 \vee M_1 \vee D_{11} D_{12}) (P_2 \\
 &\quad \vee M_2 \vee D_{21} D_{22}), \quad (44)
 \end{aligned}$$

Note that (44) contains two disjoint parts, thanks to the appearance of \bar{M}_3 in the first part and M_3 in the second part. The subfuctions of $S_1 S_2$ in the two parts now consist each of factored statistically independent entities. We substitute (44) into (41), and use disjointing techniques^{[2, 8,}

9, 53-59, 62-71] to convert the resulting expression into the Probability-Ready Expression

$$\begin{aligned}
 T &= N \vee \bar{N} \left(\bar{M}_3 (P_1 \vee \bar{P}_1 D_{11} D_{12}) (P_2 \right. \\
 &\quad \vee \bar{P}_2 D_{21} D_{22}) \\
 &\quad \vee M_3 (P_1 \\
 &\quad \vee \bar{P}_1 (M_1 \vee \bar{M}_1 D_{11} D_{12})) (P_2 \\
 &\quad \vee \bar{P}_2 (M_2 \\
 &\quad \vee \bar{M}_2 D_{21} D_{22})) \left. \right). \quad (45)
 \end{aligned}$$

The PRE (45) is converted, on a one-to-one basis, into the probability expression

$$\begin{aligned}
 "t &= n + (1 - n)((1 - m_3)(p_{-1} + (1 - \\
 & p_{-1}) d_{-11} d_{-12})(p_{-2} + (1 - \\
 & p_{-2}) d_{-21} d_{-22}) + m_3 (p_{-1} + (1 - \\
 & p_{-1})(m_{-1} + (1 - m_{-1})d_{-11} d_{-12}) (p_{-2} + (1 - \\
 & p_{-2})(m_{-2} + (1 - m_{-2}) d_{-21} d_{-22})). \quad (46)"
 \end{aligned}$$

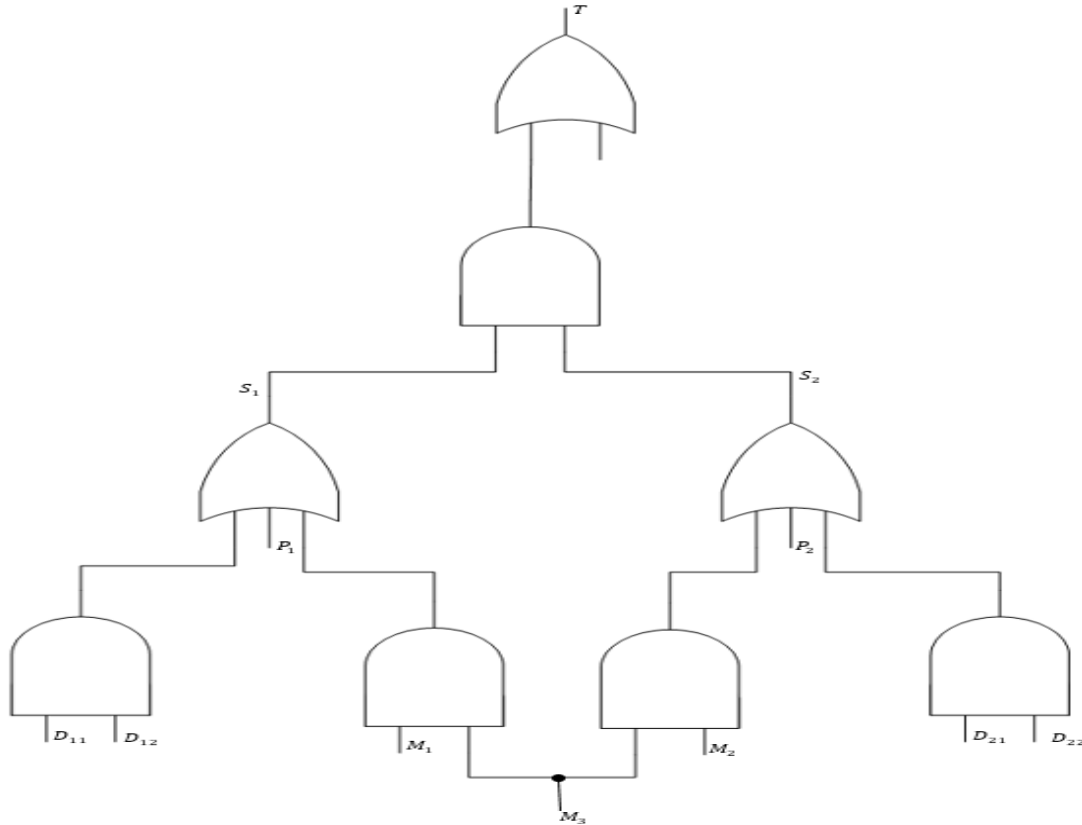


Fig. 4. Fault Tree for a multiprocessor system (taken from^[23] and^[61]).

Table 2. Values of the indicator of the top event conditioned by the indicator X of a basic event (equivalent to the Boolean quotient T/X).

X	$T X$
D_{11}	$N \vee \bar{N} (\bar{M}_3(P_1 \vee \bar{P}_1 D_{12})(P_2 \vee \bar{P}_2 D_{21}D_{22}) \vee M_3(P_1 \vee \bar{P}_1 (M_1 \vee \bar{M}_1 D_{12}))(P_2 \vee \bar{P}_2 (M_2 \vee \bar{M}_2 D_{21}D_{22})))$
P_1	$N \vee \bar{N} (\bar{M}_3(P_2 \vee \bar{P}_2 D_{21}D_{22}) \vee M_3 (P_2 \vee \bar{P}_2 (M_2 \vee \bar{M}_2 D_{21}D_{22})))$
M_1	$N \vee \bar{N} (\bar{M}_3(P_1 \vee \bar{P}_1 D_{11}D_{12})(P_2 \vee \bar{P}_2 D_{21}D_{22}) \vee M_3(P_2 \vee \bar{P}_2 (M_2 \vee \bar{M}_2 D_{21}D_{22})))$
M_3	$N \vee \bar{N} (P_1 \vee \bar{P}_1(M_1 \vee \bar{M}_1 D_{11}D_{12}) \vee (P_2 \vee \bar{P}_2 (M_2 \vee \bar{M}_2 D_{21}D_{22})))$
N	1

Table 3. The *a priori* and *a posteriori* probabilities of component failures in Example 2.

Component X	The <i>a priori</i> failure probabilities	The <i>a posteriori</i> failure probabilities of Bobbio, <i>et al.</i> , ^[10]	The <i>a posteriori</i> failure probabilities of (38)
D_{11}	$d = 0.32968$	0.98436	0.9978947
P_1	$P = 0.00025$	0.02252	0.0022937
M_1	$m = 0.000015$	0.000015	0.0000150018
M_3	$m = 0.000015$	0.000015	0.0000150034
N	$n = 0.00001$	0.000081	0.0008425

Table 2 lists the conditional indicators or Boolean quotients (T/X), where X stands for D_{11} , P_1 , M_1 , M_3 , and N . Table 3 shows the *a priori* failure probabilities assumed by Bobbio, *et al.*,^[23] and the *a posteriori* failure probabilities computed by them via Bayesian-Network modelling. Table 3 also reports a

a posteriori probabilities computed via (38), under the assumption of equal reliabilities for similar components, *i.e.*, $d = d_{11} = d_{12} = d_{21} = d_{22}$, $P = P_1 = P_2$, and $m = m_1 = m_2 = m_3$. Thanks to (38), (40) and (46), one obtains $t = E\{T\} = n + (1 - n)((1 - m)(P + (1 - P)d)^2 + m(P + (1 - P)(m + (1 - m)d)(P + (1 - P)(m + (1 - m)d^2)))$, (47)

$$E\{D|T\} = \left(\frac{d}{t}\right) \left(n + (1 - n) \left((1 - m)(P + (1 - P)d)(P + (1 - P)d^2) + m \left(P + (1 - P)(m + (1 - m)d)(P + (1 - P)(m + (1 - m)d^2)) \right) \right) \right), \tag{48}$$

$$E\{P|T\} = \left(\frac{P}{t}\right) \left(n + (1 - n) \left((1 - m)(P + (1 - P)d^2) + m(P + (1 - P)(m + (1 - m)d^2)) \right) \right), \tag{49}$$

$$E\{M_1|T\} = \left(\frac{m}{t}\right) \left(n + (1 - n) \left((1 - m)(P + (1 - P)d^2)^2 + m(P + (1 - P)(m + (1 - m)d^2)) \right) \right), \tag{50}$$

$$E\{M_3|T\} = \left(\frac{m}{t}\right) \left(n + (1-n) \left(P + (1-P)(m + (1-m)d^2)\right)^2\right), \quad (51)$$

$$E\{N|T\} = n/t. \quad (52)$$

In passing, we note that $E\{D_{11}|T\} = E\{D_{12}|T\} = E\{D_{21}|T\} = E\{D_{22}|T\} = E\{D|T\}$, $E\{P_1|T\} = E\{P_2|T\} = E\{P|T\}$.

However, $E\{M_1|T\} = E\{M_2|T\} \neq E\{M_3|T\}$.

The results obtained in Table 3 are at best intriguing. We were expecting to obtain identical or at least approximately equal results in the second column of Table 3 (the *a posteriori* failure probabilities of Bobbio, *et al.*,^[23]), and the third column of Table 3 (the *a posteriori* failure probabilities computed herein via (38)). However, while the values for D_{11} , M_1 , and M_3 are somewhat reasonably similar, the values for each of P_1 and N differ by one order of magnitude. We argue that our computations are based on a simple fault-tree model that exactly fits our needs, and hence it is preferable according to Ockham's

(Occam's) razor, which requires a model to retain the minimum of assumptions and details needed to capture all the essential features of what the model represents while excluding any extraneous or distracting features^[72]. The details of our model are visible enough to allow an interested reader to check it by verifying the derivation of our equations and reproducing our numbers with a small calculator. In particular, our *a posteriori* failure probabilities can be easily seen to pass a simple check of satisfying the following conditional-probability equation derivable from (45).

$$\begin{aligned} 1 = E\{T|T\} = E\{N|T\} + (1 - E\{N|T\}) & ((1 - E\{M_3|T\})(E\{P_1|T\} + (1 \\ - E\{P_1|T\})E\{D_{11}|T\} E\{D_{12}|T\}) & (E\{P_2|T\} + (1 \\ - E\{P_2|T\})E\{D_{21}|T\}E\{D_{22}|T\}) & + E\{M_3|T\} (E\{P_2|T\} + (1 - E\{P_2|T\})(E\{M_1|T\} + (1 \\ - E\{M_1|T\})E\{D_{11}|T\} E\{D_{12}|T\})) & (E\{D_{11}|T\} E\{D_{12}|T\}(E\{M_2|T\} + (1 \\ - E\{M_2|T\}) E\{D_{21}|T\} E\{D_{22}|T\}))) & . \end{aligned} \quad (53)$$

Conclusions

We presented and compared two kinds of *a posteriori* analysis of fault trees, namely an analysis in the probability domain, and another in the Boolean domain. The main thesis of the probability-domain FTA is that it necessitates only the *a posteriori* analysis of single gates. Therefore, we discussed the general *a posteriori* analysis of single AND or OR gates, and then

derived (under a variety of appropriate assumptions) a *a posteriori* solution for an AND gate with SI inputs, an OR gate with ME inputs, and an OR gate with SI inputs. The results obtained are applied to a detailed fault-tree example. In addition, we treated the *a posteriori* analysis of fault trees in the Boolean domain. We demonstrated that in many cases this analysis is possible via *elementary* fault-tree manipulations that use the concept of a *Boolean quotient* (known also as a Boolean ratio,

subfunction or restriction) to effectively implement Bayes' Theorem in the Boolean domain. Again, a demonstrative example was given to illustrate the Boolean *a posteriori* FTA and explain its details, and show that the power of Bayesian networks (BNs) is not really warranted in many simple (albeit significant) cases. A detailed comparison between the two kinds of *a posteriori* FTA was also given to set the stage for explaining how these two kinds can be interrelated and even combined. The essential difference between the two kinds is that the first kind takes place in the probability domain and relies on educated guessing and solution of algebraic equations, while the second kind is a novel implementation of Bayes' Theorem in the Boolean domain, and acts occasionally as a suitable alternative to using the too-powerful technique of Bayesian networks. We stress herein that results obtained via the second kind of *a posteriori* FTA are much easier to verify and replicate than those obtained via Bayesian networks.

Further research is needed to utilize the two aforementioned kinds of *a posteriori* FTA in more practical situations, and to explore the possibility of existence of other kinds of *a posteriori* FTA. The comparison between the given two kinds of *a posteriori* FTA should be extended to further interrelate and even combine them. The implementation of Bayes' Theorem in the Boolean domain warrants further investigation, and opens new avenues for pedagogical and computational applications in probability theory and reliability engineering. An urgent issue to pursue is to solve many simple as well as complicated examples via both the second kind of *a posteriori* FTA and the Bayesian-network analysis to see if they do really agree or to identify reasons of disagreement between them and to locate where

discrepancy between them emerges.

Appendix A: Boolean Quotient

Let us define a literal to be a letter or its complement, where a letter is a constant or a variable. A Boolean term or product is a conjunction or ANDing of m literals in which no letter appears more than once. For $m=1$, a term is a single literal and for $m=0$, a term is the constant 1. Note that, according to this definition the constant 0 is not a term. Given a Boolean function f and a term t , the Boolean quotient of f with respect to t , denoted by (f/t) , is defined to be the function formed from f by imposing the constraint $\{t = 1\}$ explicitly^[44], *i.e.*

$$f/t = [f]_{t=1}, \quad (A1)$$

The Boolean quotient is also known as a ratio^[40], a subfunction^[41, 43, 45-50], or a restriction^[42]. Brown^[44] lists and proves several useful properties of Boolean quotients, of which we reproduce the following ones:

$$f/1 = f, \quad (A2)$$

$$f/st = (f/s)/t = (f/t)/s, \text{ for } st \neq 0, \quad (A3)$$

$$f \leq g \Rightarrow f/t \leq g/t$$

{for n -variable functions f and g and an m -variable term t with $m \leq n$ }, (A4)

$$t \wedge f = t \wedge (f/t) \quad (A5)$$

$$\bar{t} \vee f = \bar{t} \vee (f/t) \quad (A6)$$

$$t \wedge f \leq f/t \leq \bar{t} \vee f \quad (A7)$$

In this Appendix, we followed Brown^[44] in denoting a Boolean quotient by an inclined slash (f/t) . However, in the main text we denote it by a vertical bar $(f|t)$ to stress the equivalent meaning of f conditioned by t or f given t .

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استعراض ومقارنة نوعين من التحليل اللاحق لأشجار الأخطاء

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المستخلص. إن أشجار الأخطاء هي أدوات تحليلية للاستنباط المنظم من أعلى إلى أسفل، وهي تتمتع بتطبيقات متنوعة في العديد من المجالات مثل المعولية والسلامة والأمن. ويمكن تسمية التحليل الأمامي لأشجار الأخطاء بالتحليل المسبق لأنه يتوقع احتمال الحدث الأوجي لشجرة الأخطاء بدلالة احتمالات أحداثها الأساسية. تقدم ورقة البحث هذه استعراضاً تعليمياً ومقارنة تفصيلية لنوعين من التحليل الخلفي أو اللاحق (البُعدي) لأشجار الأخطاء يتم تنفيذهما في الميدان الاحتمالي والميدان المنطقي (البولاني) على التوالي. نفترض في حالة التحليل اللاحق لأشجار الأخطاء في الميدان الاحتمالي كون احتمال الحدث الأوجي معلوماً، كأن يتأكد لنا وقوع هذا الحدث ومن ثم يصبح احتمال مساوياً للواحد الصحيح. يمضي هذا التحليل قدماً بصورة معاودة في الميدان الاحتمالي لتقدير احتمالات الأحداث الأدنى في إطار بعض الافتراضات الواقعية، مثل: التنافي أو الاستقلال الإحصائي لأحداث المدخلات لبوابة منطقية محددة، ومع الاستفادة من تخمينات حصيفة لقيم نسب معينة بين احتمالات مثل هذه الأحداث. تقدم هذه الورقة إجراءً رياضياً مفصلاً لتنفيذ هذا التحليل اللاحق لأشجار الأخطاء يعظم الانتفاع بمفهوم المزوجة. ويتجلى هذا الإجراء من خلال مثال توضيحي مفصل. تدرس الورقة أيضاً التحليل اللاحق لأشجار الأخطاء في الميدان المنطقي. وهذا التحليل متوفر في أدبيات الموضوع في صورة أداة قوية جداً تعرف باسم الشبكات الباييزية. تظهر هنا أنه في كثير من الحالات يظل هذا التحليل ممكناً عن طريق معالجات أولية لأشجار الأخطاء تستخدم مفهوم خارج القسمة البولاني (المنطقي) للتنفيذ الفعال لنظرية بايز في الميدان المنطقي. ومرة أخرى، يتم إعطاء مثال توضيحي لبيان التحليل اللاحق لأشجار الأخطاء في الميدان المنطقي، وشرح تفاصيله، وإظهار أن اللجوء لقوة الشبكات الباييزية ليس له ما يبرره حقاً في الحالات البسيطة. نورد مقارنة تفصيلية بين نوعي التحليل اللاحق لأشجار الأخطاء لإيضاح أوجه الشبه وأوجه الاختلاف بينهما.

الكلمات الدالة: شجرة الأخطاء، التحليل المسبق، التحليل اللاحق (البُعدي)، الميدان الاحتمالي، الميدان المنطقي (البولاني).