

Dr. Saud Wasly

Assistant Professor, Department of Electrical and Computer Engineering, King Abdulaziz University

Education

<i>Degree</i>	<i>Field of Study</i>	<i>Institution</i>	<i>Year</i>
Ph.D.	Real-time Embedded Systems	University of Waterloo, Canada	2018
MS	Real-time Embedded Systems	University of Waterloo, Canada	2013
BS	Electrical & Comp. Eng.	King Abdulaziz University	2007

Academic Experience

<i>From To</i>	<i>Institution</i>	<i>Rank</i>	<i>Title</i>	<i>Full or Part Time</i>
2018 - Present	King Abdulaziz University	Assistant Professor		Full Time

Non-Academic Experience (Including Consultations)

- President of Saudi Student Association in Waterloo, Canada (2016)
- Member of “Montig”: A Saudi technology transfer group, since 2011

Funded Research Projects and Patents From The Last Five Years

- *A Flexible Python-based Architectural Modeling and Simulation Framework For Real-time Systems*
- *(patent) A reliable and predictable scratchpad-centric OS for multi-core embedded systems*

Certifications and Professional Registrations**Current Membership in Professional Societies and Organizations**

<i>Society/Organization</i>	<i>Rank</i>	<i>Since</i>
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Honors and Awards

- First Place Award: The National Scientific Innovation Contest, 2009, for a project in embedded security
- First place medal from the Saudi Society of Scouting for the best scientific researcher (Encryption Methodology)

Institutional and Professional Services

- Head of IT unit, Faculty of Engineering, KAU, Present.
- Head of Saudi Student Association of Waterloo, 2016.
- Development engineer at Jeddah municipality, November 2007 - March 2008.
- Instructor at Jeddah Center for Scientific Innovation, 2007.
- Member of the first national robotic team, 2006 - 2007

Principal Publications/Presentations from the Past Five Years

- AM Albishi, SH Mirjahanmardi, AM Ali, V Nayyeri, SM Wasly, OM Ramahi, "Intelligent Sensing Using Multiple Sensors for Material Characterization", Sensors 2019
- S. Wasly and R. Pellizzoni, "Bundled Scheduling of Parallel Real-time Tasks," in Real-Time and Embedded Technology and Applications Symposium (RTAS), 2019.
- T. Garg, S. Wasly, R. Pellizzoni, and N. Kapre, "HopliteBuf: FPGA NoCs with Provably Stall-Free FIFOs," in the 2019 ACM/SIGDA International Symposium on Field-Programmable Gate Arrays - FPGA 2019.
- R. Tabish, R. Mancuso, S. Wasly, S. S. Phatak, R. Pellizzoni, and M. Caccamo, "A Real-Time Scratchpad-centric OS with Predictable Inter/Intra-Core Communication for Multi-core Embedded Systems," in Real-Time Systems, RTS, 2019.
- R. Tabish, R. Mancuso, S. Wasly, S. S. Phatak, R. Pellizzoni, and M. Caccamo, "A reliable and predictable scratchpad-centric OS for multi-core embedded systems," in the Real-Time and Embedded Technology and Applications Symposium (RTAS), 2017.
- S. Wasly, R. Pellizzoni, and N. Kapre, "HopliteRT: An efficient FPGA NoC for real-time applications," in Field Programmable Technology (ICFPT), 2017.
- R. Tabish, R. Mancuso, S. Wasly, A. Alhammad, S.S. Phatak, R. Pellizzoni, and M. Caccamo, "A Real-Time Scratchpad-Centric OS for Multi-Core Embedded Systems," in the Real-Time and Embedded Technology and Applications Symposium (RTAS), 2016.
- Alhammad, S. Wasly, and R. Pellizzoni, "Memory efficient global scheduling of real-time tasks," in the Real-Time and Embedded Technology and Applications Symposium (RTAS), 2015.
- S. Wasly and R. Pellizzoni, "Hiding memory latency using fixed priority scheduling," in Real-Time and Embedded Technology and Applications Symposium (RTAS), 2014.
- S. Wasly and R. Pellizzoni, "A Dynamic Scratchpad Memory Unit for Predictable Real-Time Embedded Systems," in Euromicro Conference on Real-Time Systems, 2013.

Recent Professional Development Activities (*Workshops, Trainings etc.*)